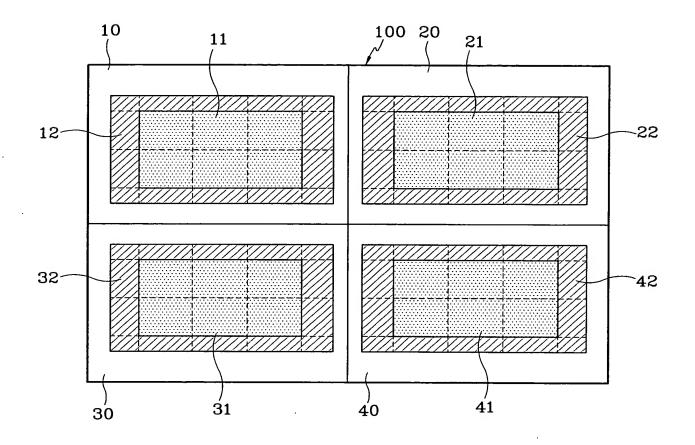
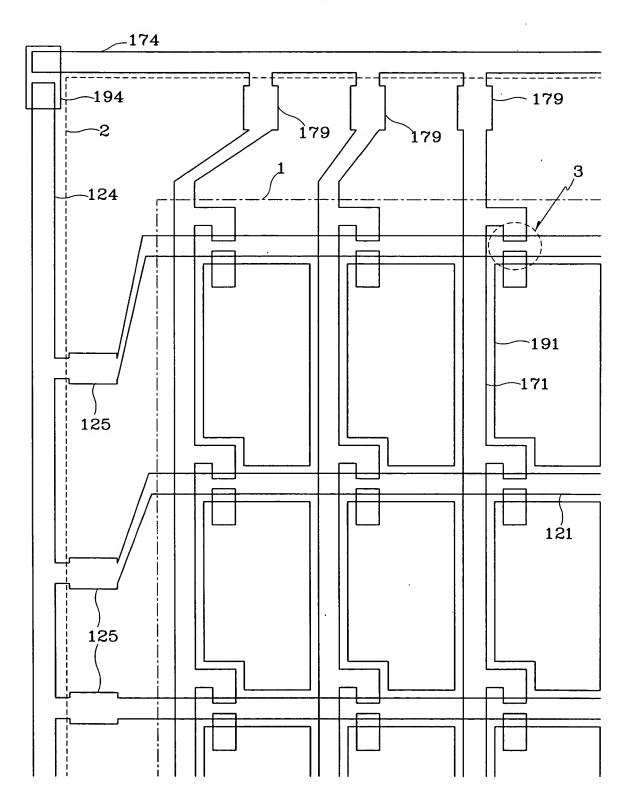
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FIG. 1



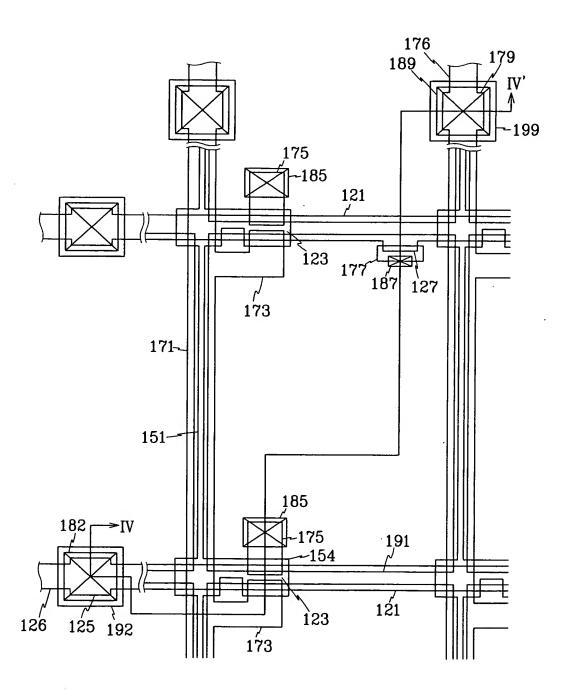
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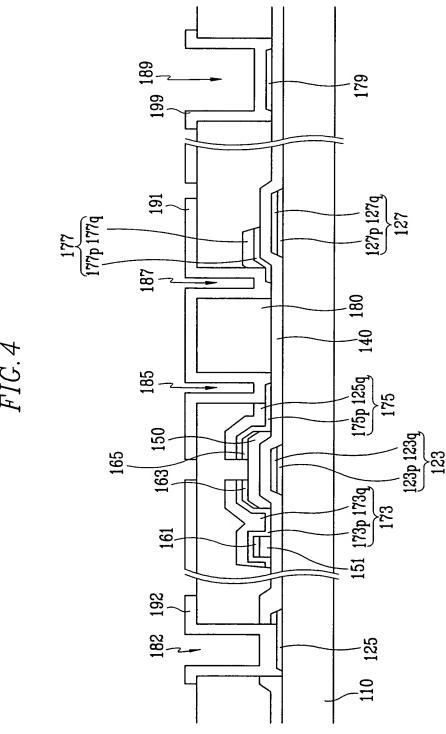
FIG.2



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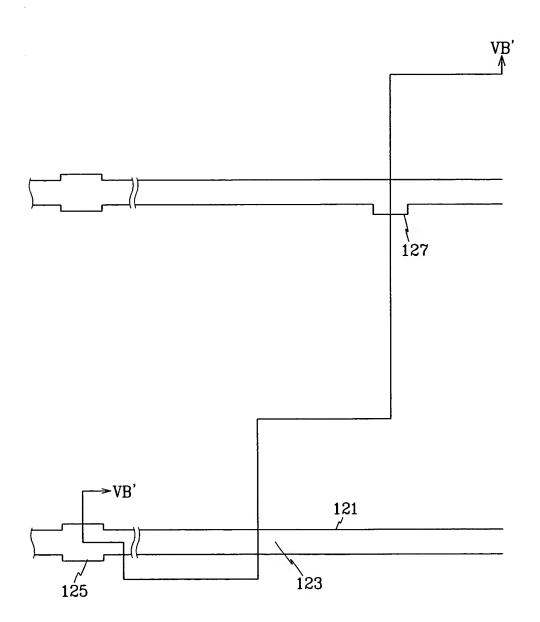
FIG. 3



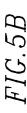


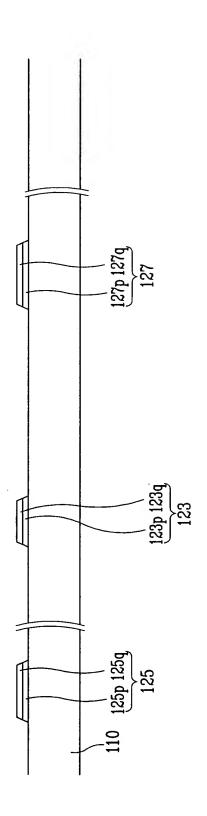
Attorney Docket No. AB-1353 US Inventors: PARK, Woon-Yong et al. Title: Thin Film Transistor Array Panel, Manufacturing Method Thereof, and Mask Therefor Sheet 5 of 54

FIG. 5A



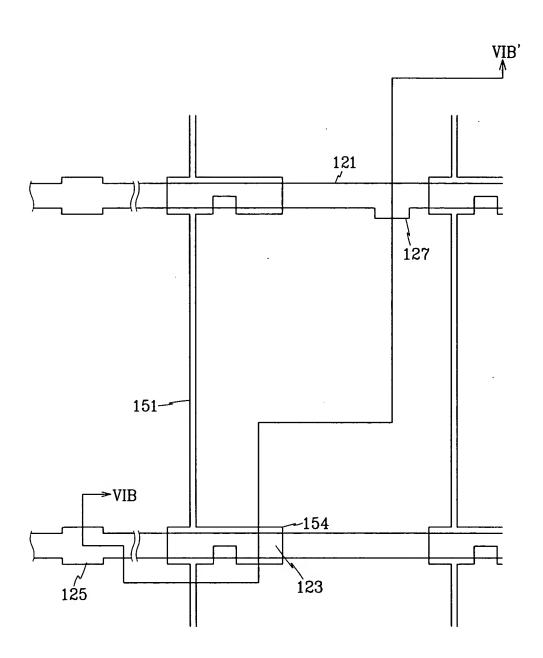
Attorney Docket No. AB-1353 US
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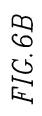


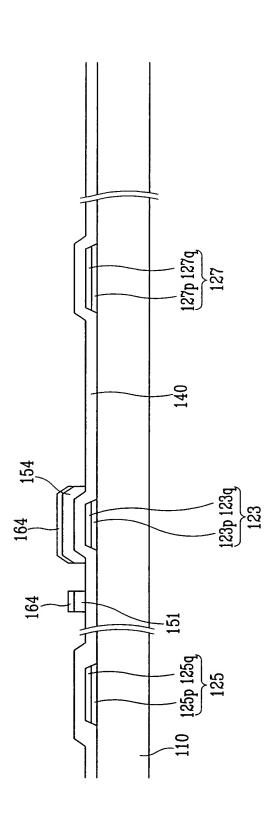


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FIG. 6A

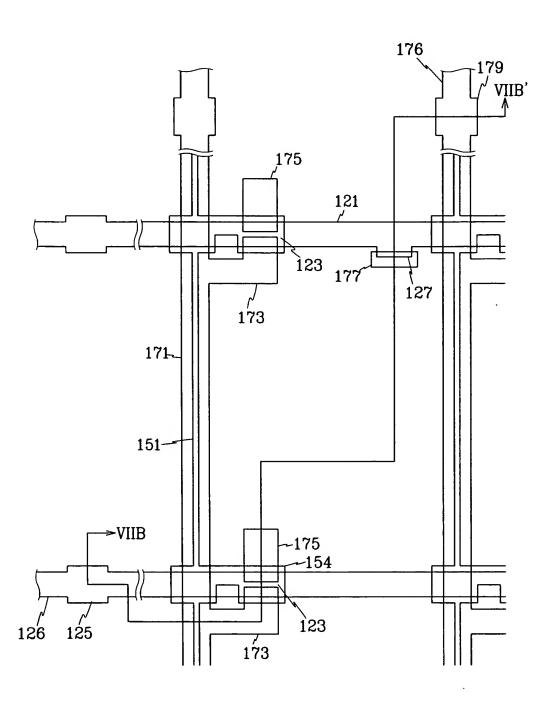


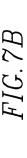


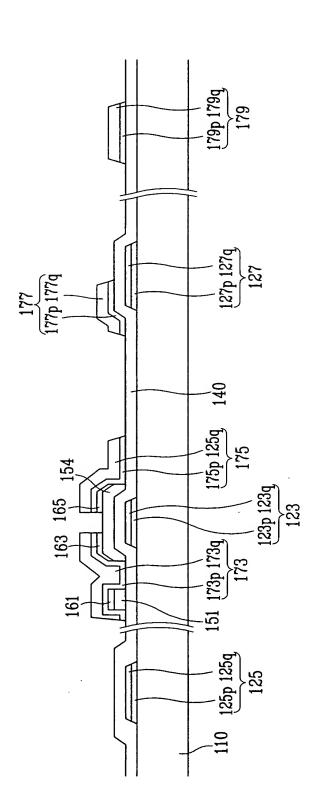


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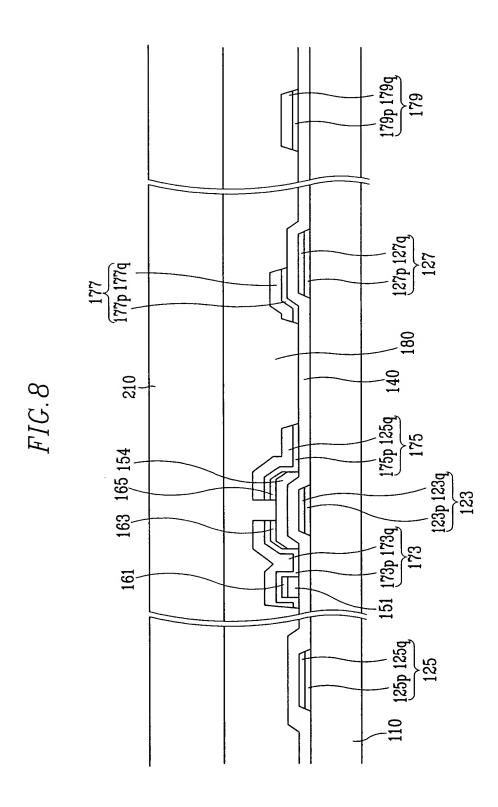
FIG. 7A





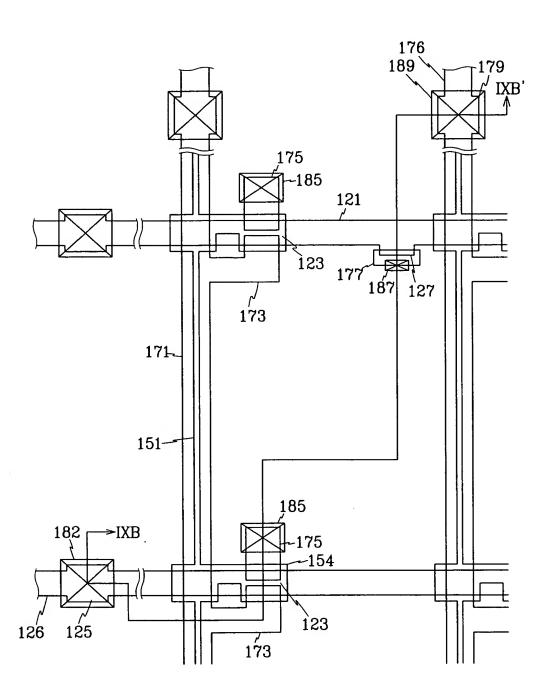


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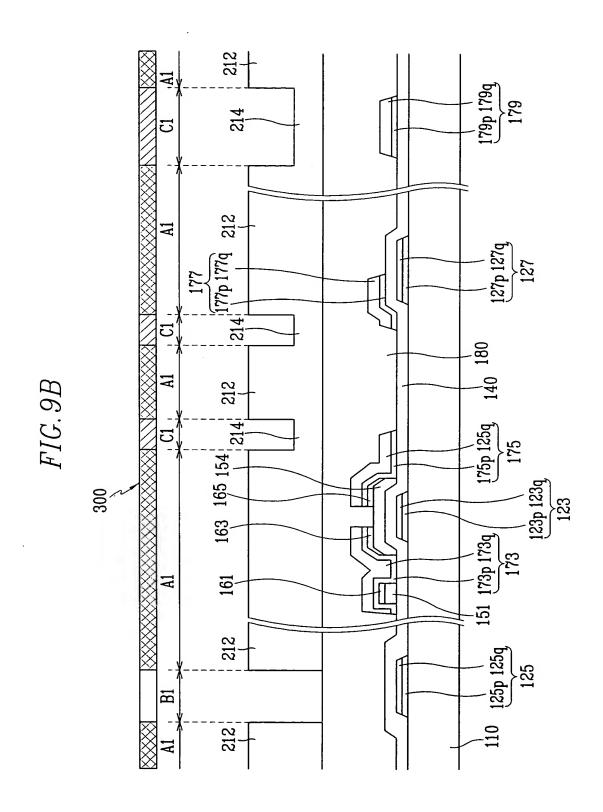


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FIG. 9A

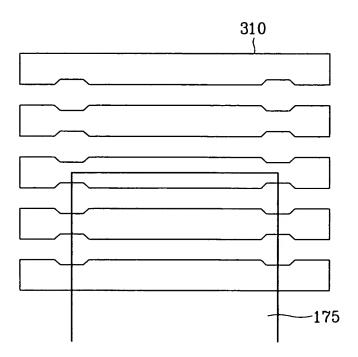


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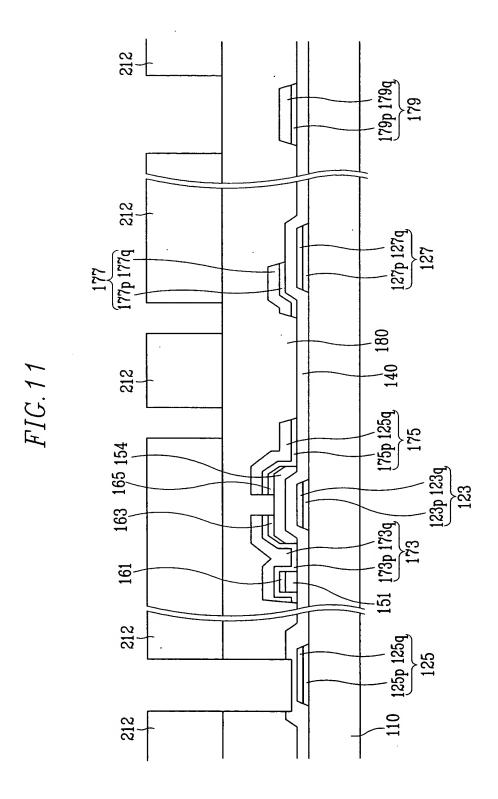


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FIG. 10



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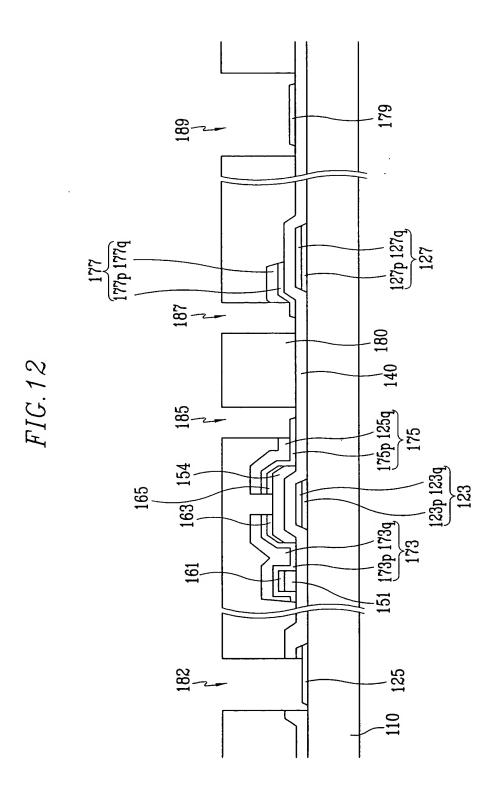
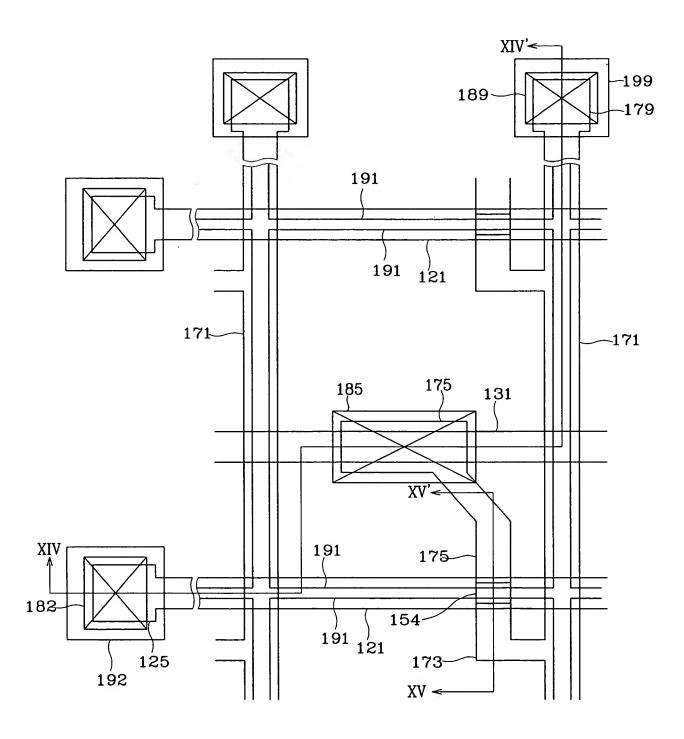


FIG. 13



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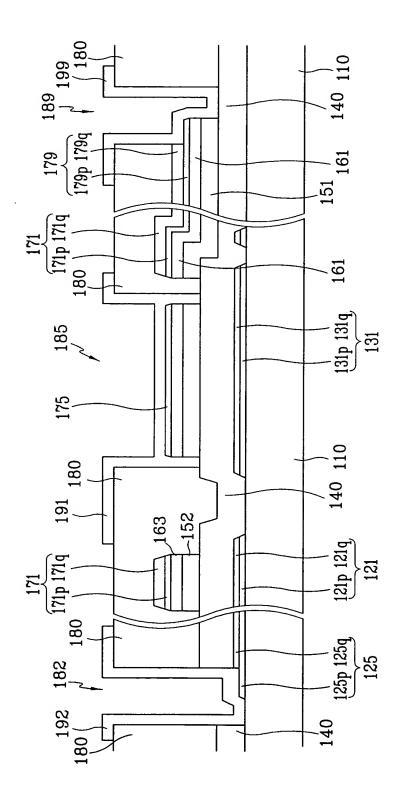
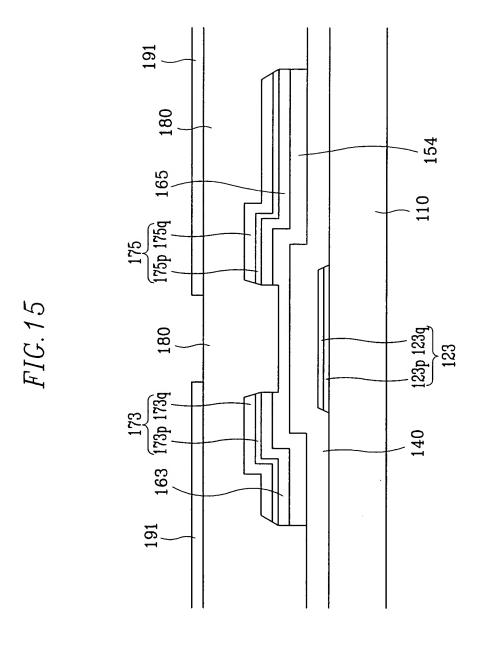


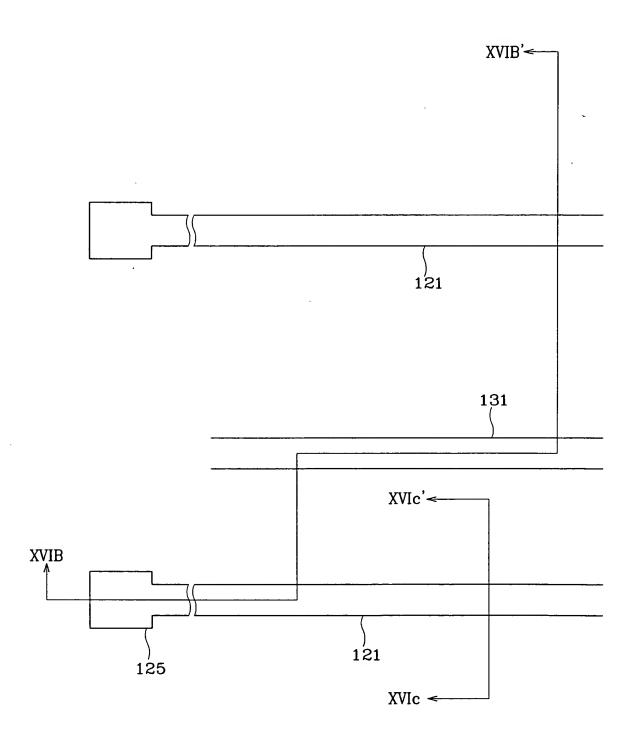
FIG. 14

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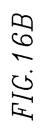


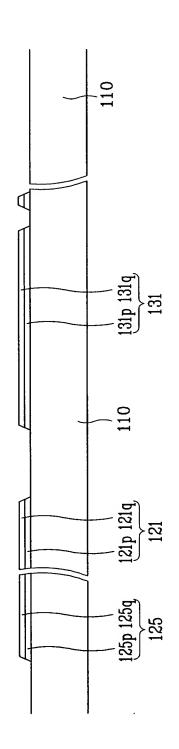
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FIG. 16A



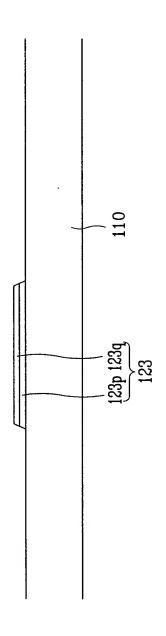
Attorney Docket No. AB-1353 US Inventors: PARK, Woon-Yong et al. Title: Thin Film Transistor Array Panel, Manufacturing Method Thereof, and Mask Therefor Sheet 21 of 54



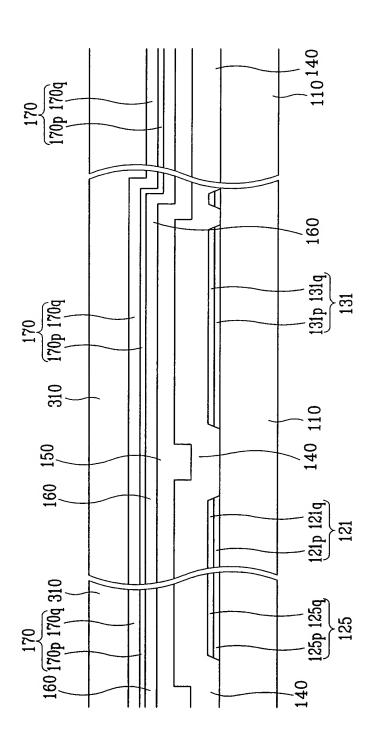


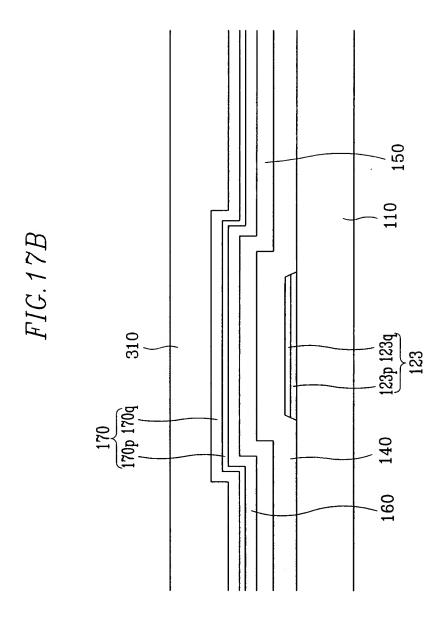
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FIG. 16C



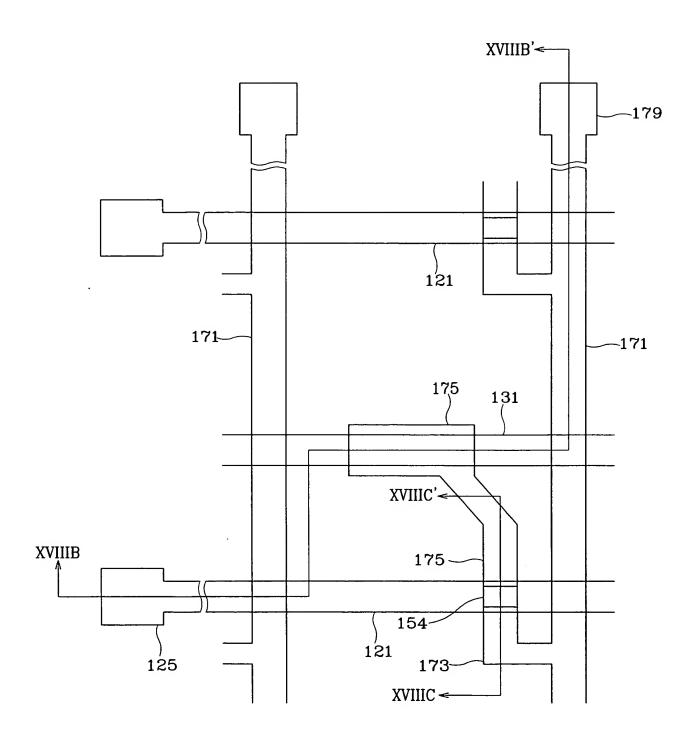






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FIG. 18A



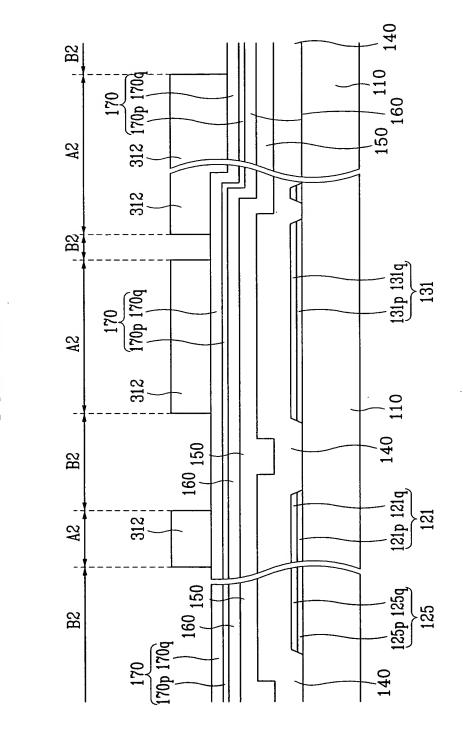
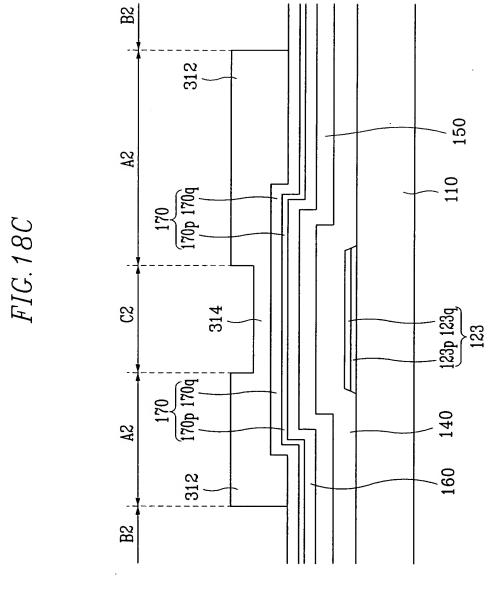
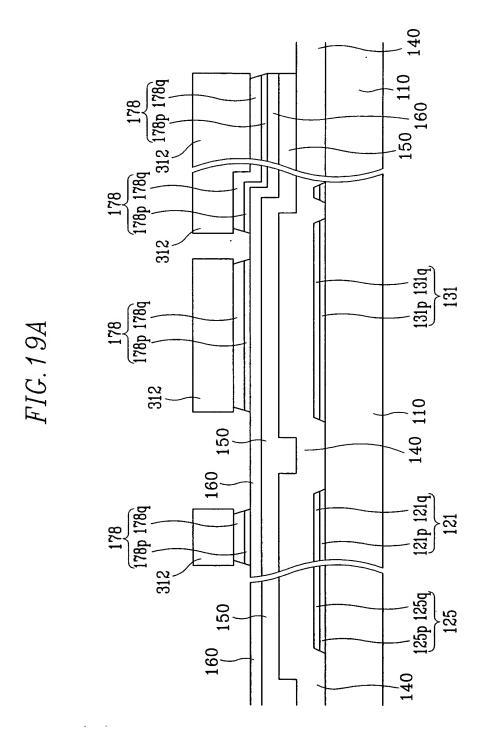
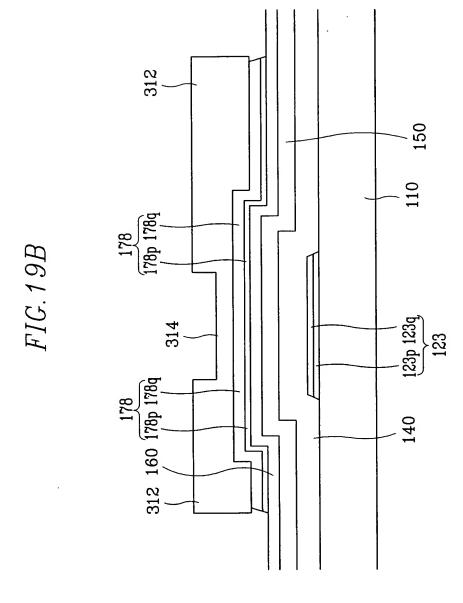
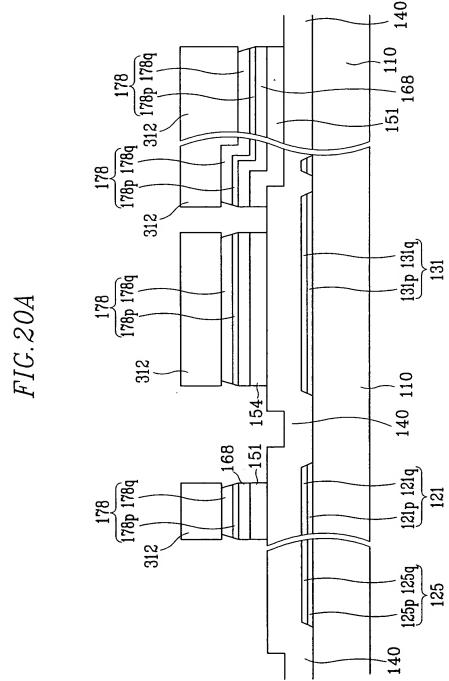


FIG. 18B

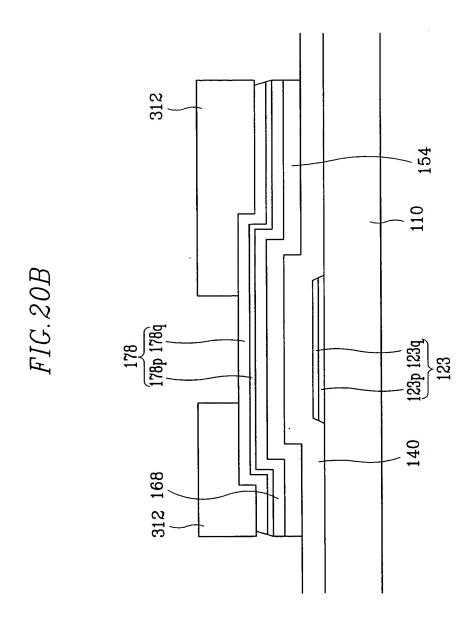


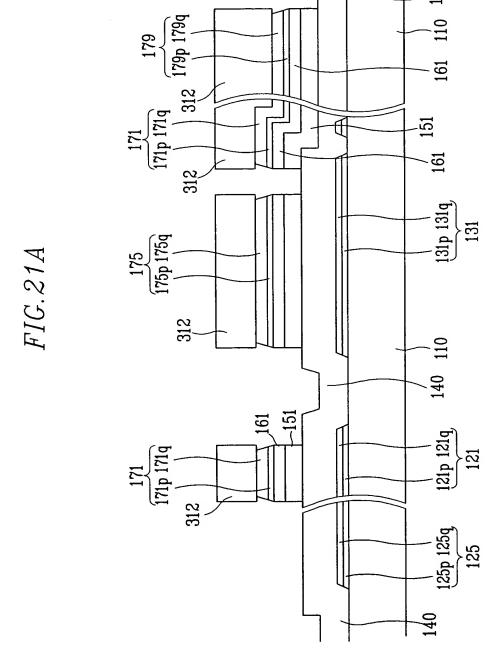


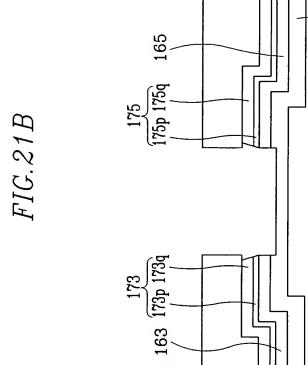




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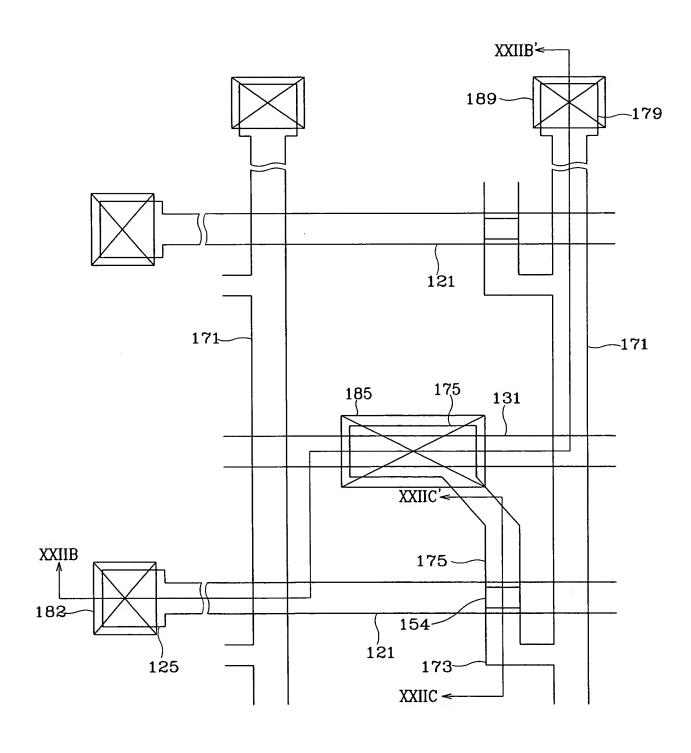




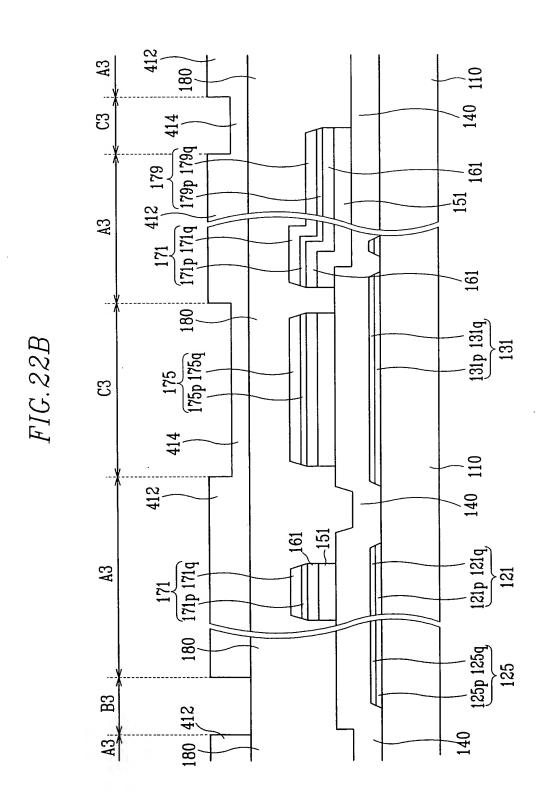


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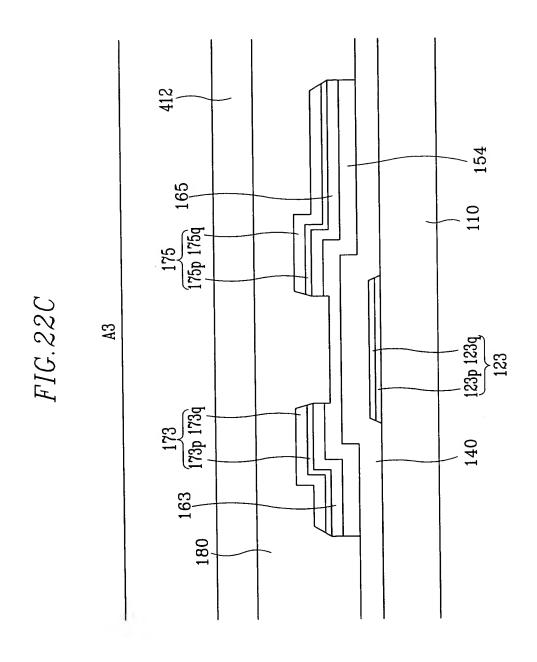
FIG. 22A

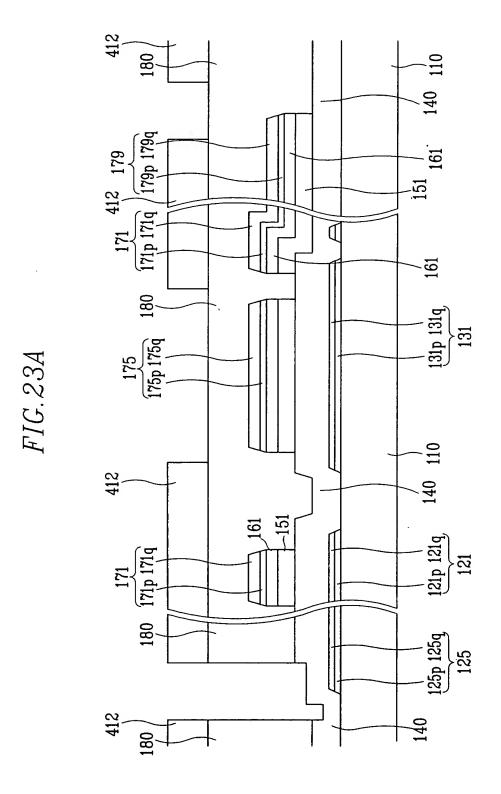


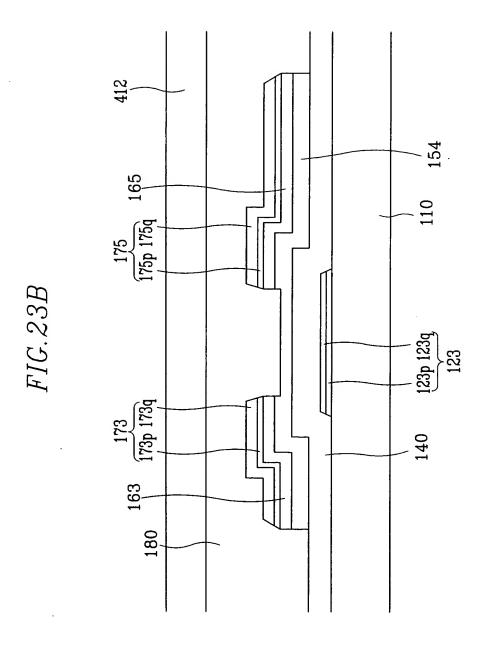
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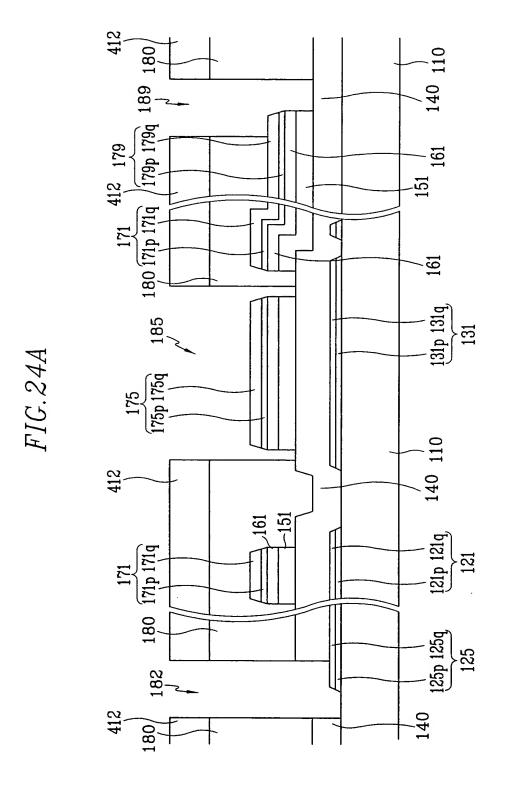


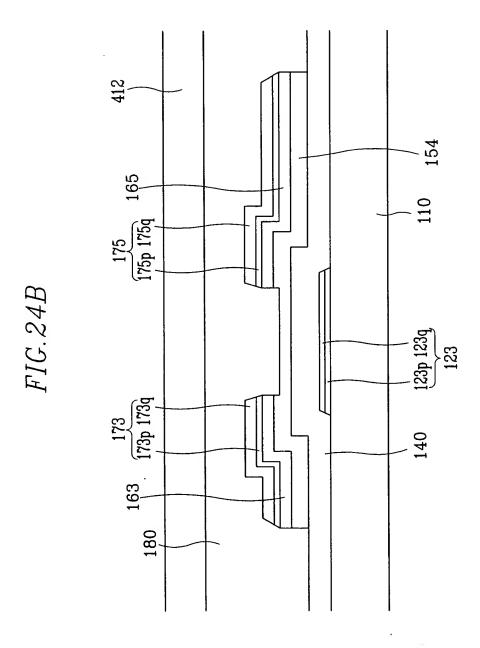
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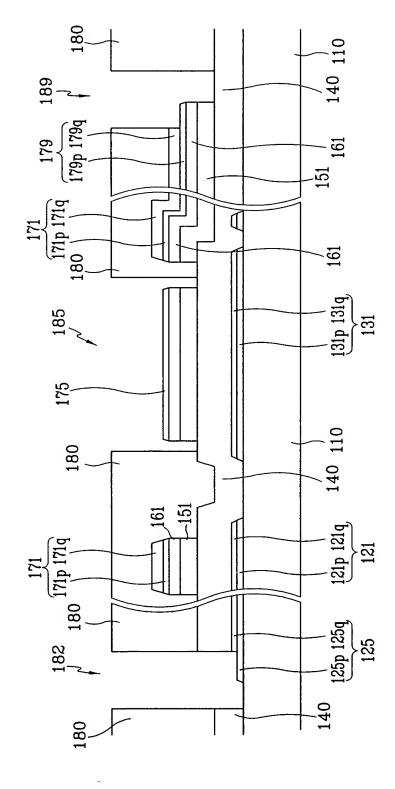
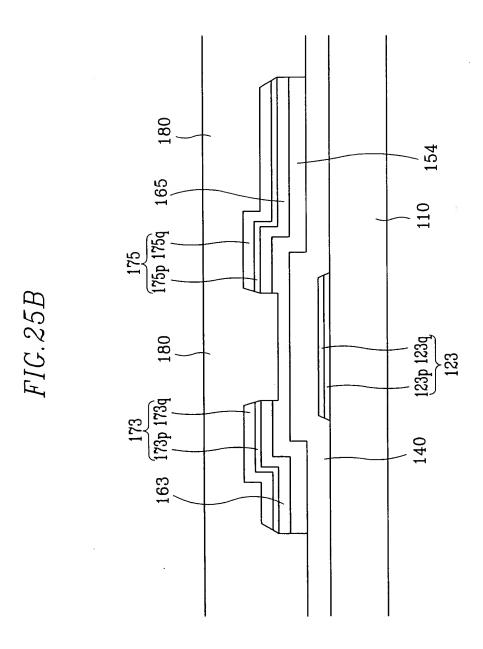


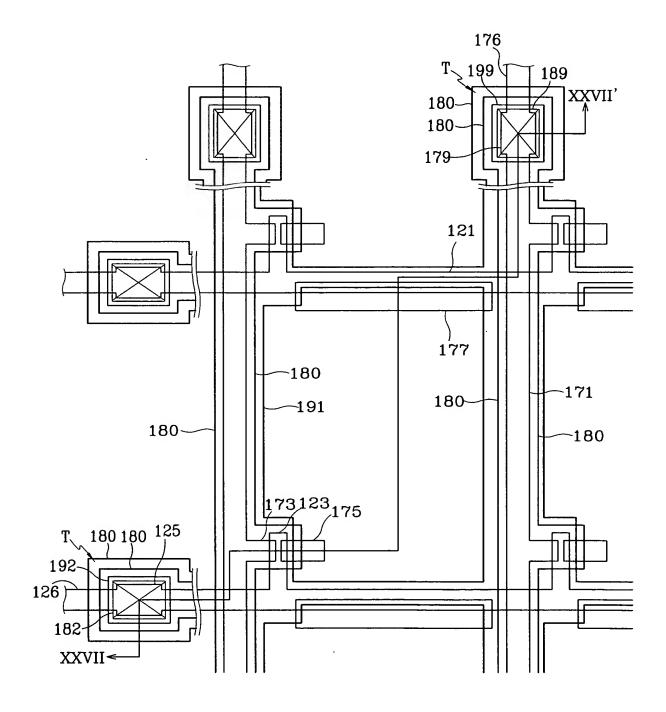
FIG. 25A

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FIG. 26



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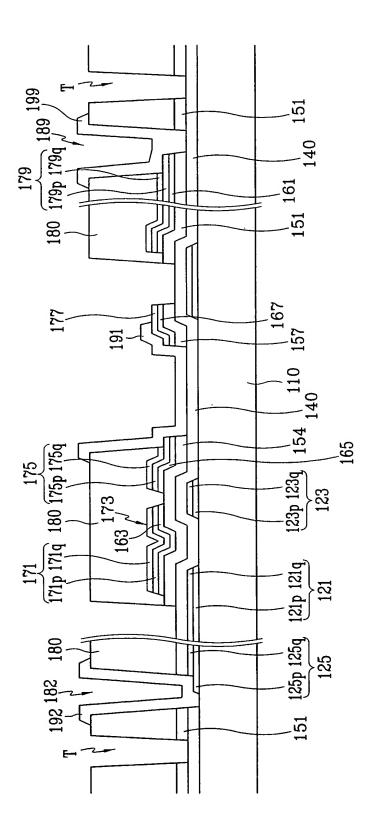
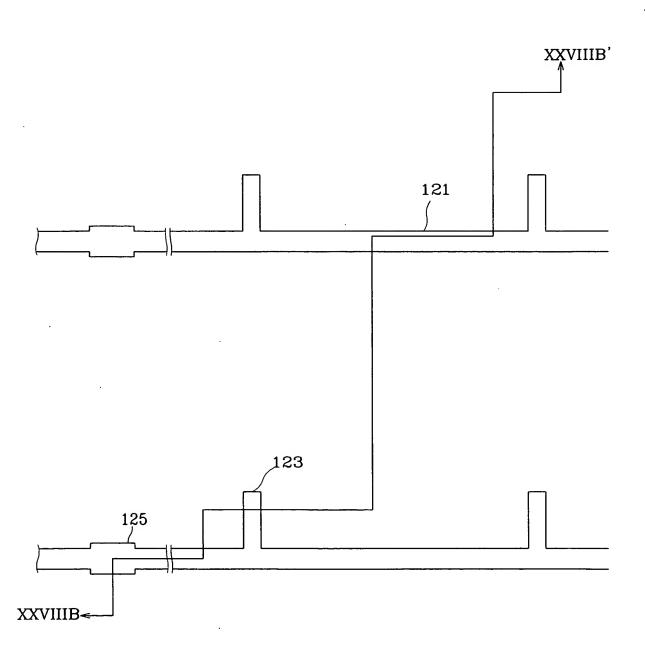


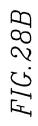
FIG.27

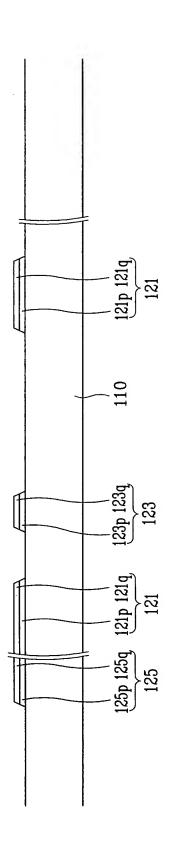
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FIG. 28A



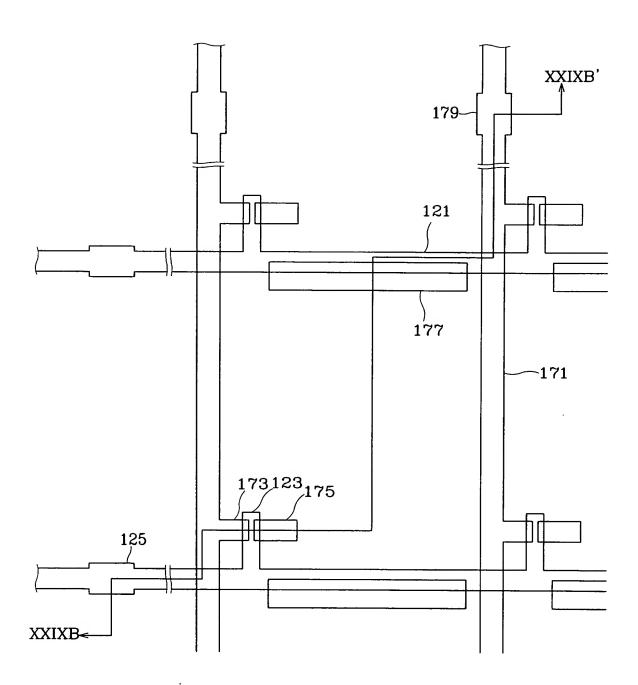
Attorney Docket No. AB-1353 US
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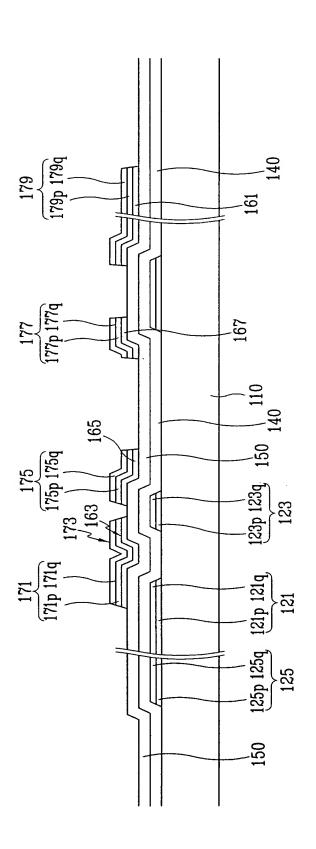


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FIG. 29A

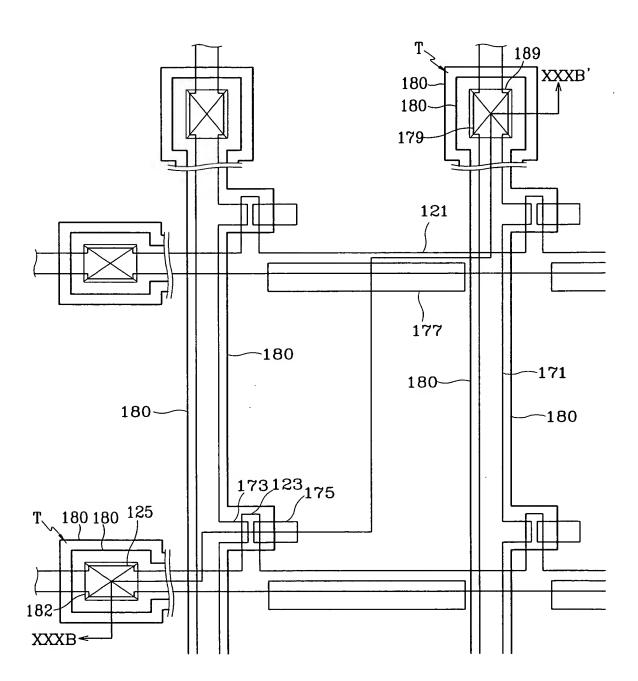






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FIG. 30A



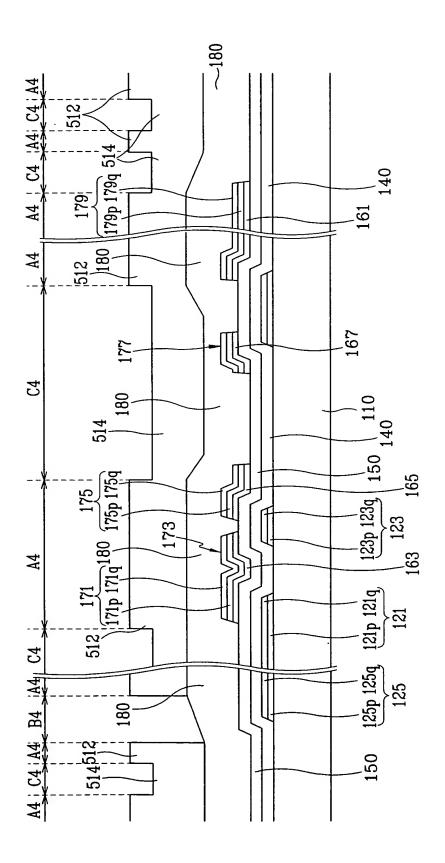


FIG. 30B

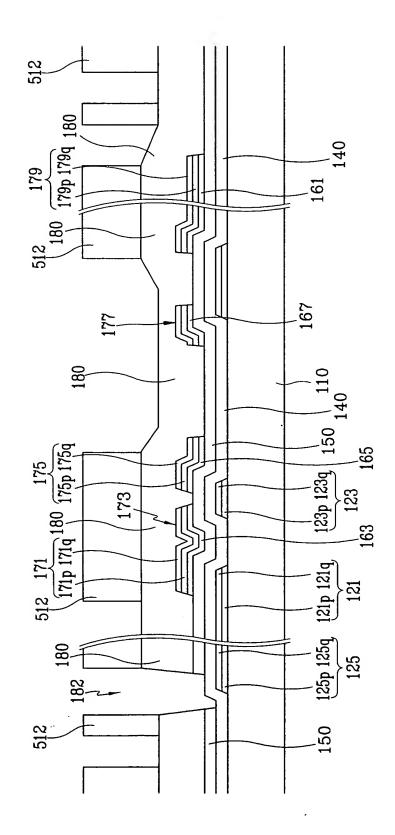


FIG.31



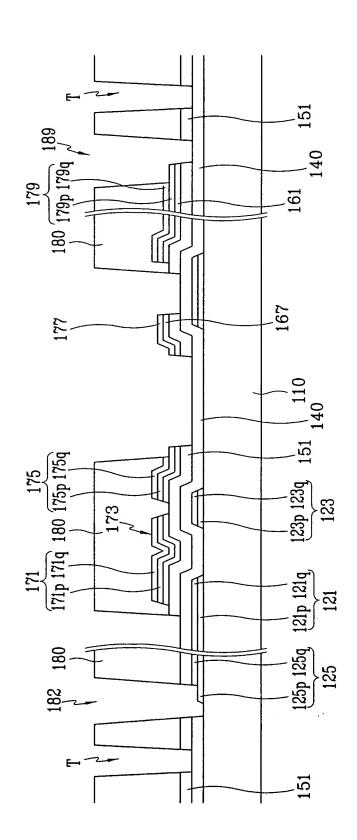
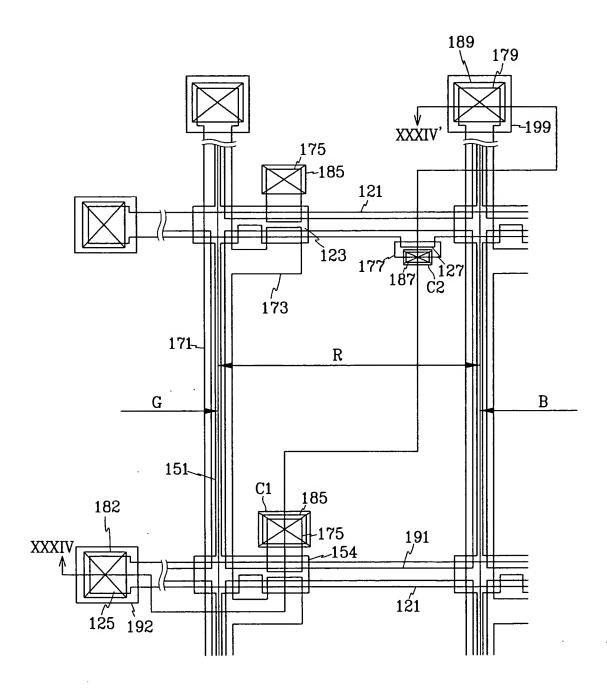


FIG. 33



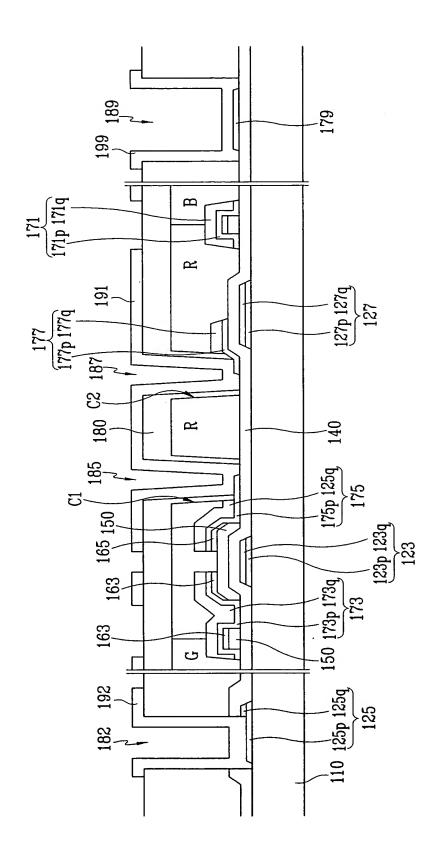


FIG. 34